Lesson Plan

 BCA – 233

COMPUTER ARCHITECTURE

Teacher name: Neelam

October 2020

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| Basic Computer Organization0 and Design: Instruction Codes, Computer registers, Computer Instructions, Timing and Control, Instruction Cycle, Memory reference instructions, Input-Output and Interrupt, Design of Basic computer, Design of accumulator logic  |

November 2020

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| Register Transfer and Micro-operations: Register Transfer Language (RTL), register transfer, Bus and Memory Transfers, Arithmetic Micro operations, Logic Micro operations, Shift Micro operations, Arithmetic Logic Shift Unit,  |

December 2020

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| Micro programmed Control: Control memory; address sequencing, micro program sequencer, Design of Control Unit UNIT -III Central Processing Unit: General registers Organization, Stack Organization, |

January2021

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| Instruction formats, Addressing Modes, Data Transfer and Manipulation, Program Control, Program Interrupt, RISC, CISC. Memory Organization: Memory hierarchy, Auxiliary Memory, Associative Memory,  |

February 2021

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| Interleaved memory, Cache memory, Virtual Memory, Memory Management Hardware, Input Output Organization : Peripheral devices , Input-Output Interface, Asynchronous data transfer, Modes of Transfer, Priority Interrupt, Direct Memory Access(DMA),Input-Output Processor(IOP). |