Lesson Plan

**Semester-V**

**Course: B.Sc (Electronics)**

**Paper Code: Paper- I**

**Nomenclature: - Microprocessor Architecture and Programming-I**

Teacher name: SK Bathla

October 2020

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| Simple Idea of three state switch & three state bus ,SAP-I (Simple as Possible )Computer, Architecture, Instruction Set, Programming SAP-1, Fetch Cycle,Execution CycleRef: 1 Digital Computer Electronics- A P Malvino (2nd Edition) |

November 2020

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| SAP-II Architecture, Instruction set of SAP –II Computer (Memory Reference instructions, Register Instructions, Jump and Call instructions, and Logic instructions) Machine Cycle and Instruction Cycle, Addressing Modes, Instruction Types. |

December 2020

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| Delay Calculations, SAP-III programming Model, MOV & MVI, arithmetic instructions, increments, decrements and rotates, logic instructions, Arithmetic and logical immediate, jump instructions,  |

January2021

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| extended register instructions, indirect instructions, stack instructions,Architecture of 8085 Microprocessor, Pin Description of 8085,  |

February 2021

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| Instruction set of 8085, Fetching and Executing Instructions, Idea of fetch execute overlap |