Lesson Plan

**B.Sc II Sem Electronics**

**Paper: I**

**Nomenclature: - Electronic Devices and Circuits –II**

Teacher name: Mr. SK Bathla

April 2021

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| Why Bias a Transistor, Selection of Operating Point, Need for Bias Stabilization, Requirement of a Biasing Circuit, Different Biasing Circuits: Fixed-Bias Circuit, Collector-to-base Bias Circuit.**Unit-II****Transistor Biasing Techniques-II:-**  Bias Circuit with Emitter Resistor, Voltage Divider Biasing Circuit, Emitter-Bias Circuit, |

May 2021

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| Gain of a multi-stage amplifier.**Unit-III****Coupling Techniques:-** How to couple two stages, Resistance-Capacitance Coupling, Transformer Coupling, Direct Coupling, Frequency Response Curve of an RC-Coupled Amplifier of two stage: Fall of Gain in Low-frequency Range, |

June 2021

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| fall in gain of high Frequencies, Bandwidth of an amplifier.**Unit-IV****Field Effect Transistor:-** Junctions Field Effect Transistor, Qualitative Description of JFET, Drain and transfer characteristics of JFET, FET small signal low frequency model, CS & CD low frequency model |

July 2021

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| MOSFET -Depletion and enhancement and their drain & transfer characteristics, CMOS( Basic idea). |