Lesson Plan

**Electronics III Semester**

**Paper: II - Digital Electronics-II**

Teacher name: Mr. SK Bathla

October 2020

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| Multiplexers, Demultiplexer, Decoder, Encoder, Parity bit generator and checker, Code Converter: BCD to Seven Segment, BCD to Cyclic Code, Binary to Decimal, Binary to Gray, Binary to Excess-3, |

November 2020

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| Application of combinational circuit: adder circuit using Multiplexers, Boolean expression implementation using Multiplexer, Boolean expression implementation using Demultiplexer,  Basic Sequential circuit, Asynchronous and Synchronous circuits, RS FF and JK Flip Flop, |

December 2020

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| Race Around Condition, Master Slave JK flip flop, T and D Flip Flop, Excitation Table, Conversion of Flip Flop, State Diagram.  Asynchronous Binary Counters, Asynchronous Mod-N Counter, |

January2021

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| Synchronous counter: Design principle of Modulo- N Counters, UP-Down counters, Decade Counter, BCD Counter.  Shift Registers, Serial-in serial out (SISO), Serial-in-parallel out (SIPO), parallel-in-serial-out (PISO) parallel-in-parallel-out (PIPO), |

February 2021

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| Bi-directional shift register, Applications of shift register:Ring counter, Johnson Counter, Time delay, Sequence Generator |