Lesson Plan

BCA-122 Logical Organization of Computers – II

Teacher name:

April 2021

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| Sequential Logic: Characteristics, Flip-Flops, Clocked RS, D type, JK, T type and MasterSlave flip-flops. State table, state diagram. Flip-flop excitation tables |

May 2021

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| Sequential Circuits: Designing registers – Serial Input Serial Output (SISO), Serial Input Parallel Output (SIPO), Parallel Input Serial Output (PISO), Parallel Input Parallel Output (PIPO) and shift registers. Designing counters – Asynchronous and Synchronous Binary Counters, Modulo-N Counters and Up-Down Counters |

June 2021

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| Memory & I/O Devices: Memory Parameters, Semiconductor RAM, ROM, Magnetic and Optical Storage devices, Flash memory, I/O Devices and their controllers. Instruction Design & I/O Organization: Machine instruction, Instruction set selection, Instruction cycle, Instruction Format and Addressing Modes. I/O Interface, Interrupt structure, Program-controlled, Interrupt-controlled & DMA transfer |

July 2021

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|  I/O Channels, IOP. |